

IEEE COMPUTER SOCIETY ANNUAL SYMPOSIUM ON VLSI

CALL FOR PAPERS

<http://www.isvlsi.org>

July 9-11, 2014
Tampa, Florida, USA



This Symposium explores emerging trends and novel ideas and concepts in the area of VLSI. The Symposium covers a range of topics: from VLSI circuits, systems and design methods to system level design and system-on-chip issues, to bringing VLSI experience to new areas and technologies like nano- and molecular devices, MEMS, and quantum computing. Future design methodologies will also be one of the key topics at the Symposium, as well as new CAD tools to support these technologies. For the past two decades, the Symposium has been a unique forum for promoting visionary approaches to VLSI design process and has been an esteemed venue for presenting multidisciplinary research. The Symposium is bringing together leading scientists and researchers from academia and industry. The papers from this Symposium have been published as the special issues of top archival journals. This fact indicates a very high quality of the Symposium papers, and we are determined to keep a strong emphasis on this critical aspect of the conference. **The Symposium proceedings are published by IEEE Computer Society Press.** The Symposium has established a reputation in bringing well-known international scientists as invited speakers, and this trend will continue.

Contributions are sought in the following areas

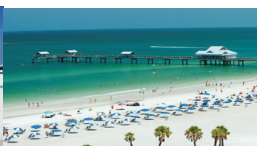
Emerging Trends in VLSI, Nanoelectronics, Molecular, Biological and Quantum Computing, MEMS, VLSI Circuits and Systems, Field-programmable and Reconfigurable Systems, System Level Design, System-on-a-Chip Design, Application-Specific Low Power, VLSI System Design, System Issues in Complexity, Low Power, Heat Dissipation, Power Awareness in VLSI Design, Test and Verification, Mixed-Signal Design and Analysis, Electrical/Packaging Co-Design, Physical Design, Intellectual property creating and sharing.

The Symposium program will include contributed papers and speakers invited by the Committee. The keynote addresses, panels and special sessions are planned as well. Authors should submit their original work using the web based submission system. **Initial submissions to the conference are limited to six pages.** Manuscripts in PDF format should be submitted using the link: <https://www.easychair.org/conferences/?conf=isvlsi2014>

Paper submission deadlines

Submission Deadline: February 16, 2014
Acceptance Notification: April 20, 2014
Submission of Final Version: May 13, 2014

Selected papers from ISVLSI 2014 will be invited for submission to a journal special issue. The selection process is based on reviewer feedback and quality of conference presentation.



ISVLSI
2014

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