

IEEE COMPUTER SOCIETY ANNUAL SYMPOSIUM ON VLSI

ISVLSI
2015

CALL FOR PAPERS

www.isvlsi.org

Laboratoire
Informatique
Robotique
Microélectronique
Montpellier

**July 8-10, 2015
Montpellier, France**



This Symposium explores emerging trends and novel ideas and concepts in the area of VLSI. The Symposium covers a range of topics: from VLSI circuits, systems and design methods to system level design and system-on-chip issues, to bringing VLSI experience to new areas and technologies like nano- and molecular devices, MEMS, and quantum computing. Future design methodologies will also be one of the key topics at the workshop, as well as new CAD tools to support them. Over almost two decades the symposium has been an unique forum promoting multidisciplinary research and new visionary approaches in the area of VLSI. The Symposium is bringing together leading scientists and researchers from academia and industry. The papers from this symposium have been published as the special issues of top archival journals. This fact indicates a very high quality of the symposium papers, and we are determined to keep a strong emphasis on this critical aspect of any conference. [The symposium proceedings are published by IEEE Computer Society Press.](#) The Symposium has established a reputation in bringing well-known international scientists as invited speakers, and this trend will continue.



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Contributions are sought in the following areas

Emerging Trends in VLSI, Nanoelectronics, Molecular, Biological and Quantum Computing, MEMS, VLSI Circuits and Systems, Field-programmable and Reconfigurable Systems, System Level Design, System-on-a-Chip Design, Application-Specific Low Power, VLSI System Design, System Issues in Complexity, Low Power, Heat Dissipation, Power Awareness in VLSI Design, Test and Verification, Mixed-Signal Design and Analysis, Electrical/Packaging Co-Design, Physical Design, Intellectual property creating and sharing.

The Symposium Program will include contributed papers and speakers invited by the Program Committee, as well as a poster session. The keynote addresses, panels and special sessions are planned as well. Authors should submit their original work using the web based submission system. Initial submissions to the conference are limited to six pages. ISVLSI uses double blind review mechanism and authors information should not be provided in the initial manuscript. All submissions should be in PDF format.

Paper submission deadlines

Submission site: <https://easychair.org/conferences/?conf=isvlsi2015>
Special Session Proposal Deadline: January 20, 2015
Paper Submission Deadline: February 25, 2015
Acceptance Notification: April 20, 2015
Submission of Final Version: May 13, 2015

Selected papers from ISVLSI 2015 will be invited for submission to a journal special issue. The selection process is based on reviewer feedback and quality of conference presentation

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