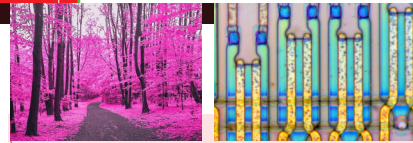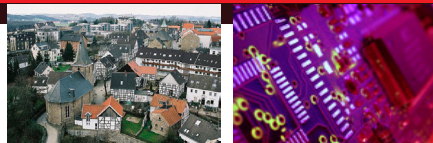# IEEE COMPUTER SOCIETY ANNUAL SYMPOSIUM ON VLSI

ISVLSI 2017

CALL FOR PAPERS

www.isvlsi.org

July 3-5, 2017
Bochum, Germany

The Symposium explores emerging trends and novel ideas and concepts covering a broad range of topics in the area of VLSI: from VLSI circuits, systems and design methods, to system level design and system-on-chip issues, to bringing VLSI methods to new areas and technologies like nano- and molecular devices, MEMS, and quantum computing. Future design methodologies are also one of the key topics at the Symposium, as well as new EDA tools to support them. Over three decades the Symposium has been a unique forum promoting multidisciplinary research and new visionary approaches in the area of VLSI, bringing together leading scientists and researchers from academia and industry. The Symposium Proceedings are published by IEEE Computer Society Press. Papers from past ISVLSI editions have been subsequently published in special issues of top archival journals which indicates the general high quality of the papers. The Symposium has established a reputation in bringing together well-known international scientists as invited speakers; the emphasis on high quality will continue at this and future editions of the Symposium.

Contributions are sought in the following areas

Emerging Trends in VLSI, Nanoelectronics, Molecular, Biological and Quantum Computing, MEMS, VLSI Circuits and Systems, Field-programmable and Reconfigurable Systems, System Level Design, System-on-a-Chip Design, Application-Specific Low Power, VLSI System Design, System Issues in Complexity, Low Power, Heat Dissipation, Power Awareness in VLSI Design, Test and Verification, Mixed-Signal Design and Analysis, Electrical/Packaging Co-Design, EDA, Physical Design, creating and sharing IP.

The Symposium Program will include contributed papers and speakers invited by the Program Committee, as well as a poster session. Keynote addresses, panels and special sessions are planned as well. Authors should submit their original work using the web based submission system. Initial submissions to the conference are limited to six pages. All submissions should be in PDF format.

Paper submission deadlines

Submission Deadline: February 24, 2017
Acceptance Notification: April 15, 2017
Submission of Final Version: May 12, 2017

Selected papers from ISVLSI 2017 will be invited for submission to a journal special issue. The selection process is based on reviewer feedback and quality of conference presentation

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