IEEE COMPUTER SOCIETY ANNUAL SYMPOSIUM ON VLSI ALL FOR LATE BREAKING RESEARCH BR) PAPERS: 2-4 Pages Limit



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Accepted LBR Papers will be Published in IEEE Xplore.

Topics of Interest for LBR Papers:

- Circuits, Reliability, and Fault-Tolerance (CRT): analog/mixed-signal circuits design and testing, RF and communication circuits, adaptive circuits and interconnects, design for testability, online testing techniques, static and dynamic defect- and fault- recoverability, variation aware design, VLSI aspects of sensor and sensor network.
- Computer-Aided Design and Verification (CAD): hardware/software codesign, logic and behavioral synthesis, simulation and formal verification, physical design, signal integrity, power and thermal analysis, statistical approaches.
- Digital Circuits and FPGA based Designs (DCF): digital circuits, chaos/neural/fuzzy-logic circuits, high speed/low-power circuits, energy efficient circuits, near and sub-threshold circuits, memories, FPGA designs, FPGA based systems.
- Emerging and Post-CMOS Technologies (EPT): nanotechnology, molecular electronics, quantum devices, optical computing, spin-based computing, biologically-inspired computing, CNT, SET, RTD, QCA, reversible logic, and CAD tools for emerging technology devices and circuits.
- System Design and Security (SDS): structured and custom design methodologies, microprocessors/micro-architectures for performance and low power, embedded processors, analog/digital/mixed-signal systems, NoC, power and temperature aware designs, hardware security, cryptography, watermarking, and IP protection, TRNG and security-oriented circuits, PUF circuits.
- VLSI for Applied and Future Computing (AFC): Neuromorphic and braininspired computing, quantum computing, circuits and architectures for machine learning and artificial intelligence, methodologies for on-chip learning, deep learning acceleration techniques, applications for and usecases of learning systems, sensor and sensor network, electronics for Internet of Things and smart medical devices.

Authors are invited to submit original, unpublished manuscripts in IEEE proceedings format (2-4 Pages). To enable blind review, the author information should be omitted from the submission. The manuscript as a single PDF is to be submitted online through Easychair in LBR Track.

Paper submission deadlines

Paper Submission Deadline: April 22, 2024 Acceptance Notification: April 30, 2024 Submission of Final Version: May 6, 2024











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