IEEE COMPUTER SOCIETY ANNUAL SYMPOSIUM ON VLSI

CALL FOR PAPERS: STUDENT RESEARCH FORUM

www.ieee-isvlsi.org

ISVLSI 2024

July 1 - 3, 2024
Knoxville, TN, USA

The 2024 Symposium explores emerging trends and novel ideas and concepts covering a broad range of topics in the area of VLSI: from VLSI circuits, systems and design methods, to system level design issues, to bringing VLSI design to new areas and technologies such as nano- and molecular devices, security, artificial intelligence, and Internet-of-Things, etc. Future design methodologies and new EDA tools are also a key topic at the Symposium. Over three decades the Symposium has been a unique forum promoting multidisciplinary research and new visionary approaches in the area of VLSI, bringing together leading scientists and researchers from academia and industry. The ISVLSI proceedings will be indexed in the IEEE Xplore Digital Library. Selected high quality papers will be further invited for submission to a journal special issue. The Symposium has established a reputation in bringing together well-known international scientists as invited speakers. The emphasis on high quality will continue at this and future editions of the Symposium.

Call for Papers: Student Research Forum

The ISVLSI 2024 will host a Student Research Forum (SRF), providing opportunities for recently graduated or final phase BS/MS/Ph.D. students to present their research/thesis work to a broad audience in the VLSI community from both industry and academia. The Student Research Forum will help students to establish contacts for entering the job market. Additionally, representatives from industry and academia will get a glance at cutting-edge research performed by the future leaders in the VLSI community.

Eligibility:
- Students who are currently enrolled in a PhD program, or they have finished their BS/MS dissertation/thesis within the last 12 months.
- The research topic should be of relevance to the ISVLSI community.
- Students who have presented in prior ISVLSI Student Research Forum, must clearly indicate additional (**70%**) new technical content compared to the previous submission.

Submission Guidelines

Please submit a paper in IEEE conference format describing the problem addressed in the thesis, novelty of the solution, observations and analysis. Focus should be on scientific challenges, the originality and quality of the solution.
- The papers have a 4-page limit.
- The submission can include author information (student and advisor).
- A single PDF file should be submitted via EasyChair under the track of Student Research Forum. (https://easychair.org/conferences/?conf=isvlsi2024)

Benefits to the selected researcher:
- 4-page publication in the ISVLSI 2024 proceedings (indexed in IEEE Xplore).
- Presentation in the Student Research Forum poster session.
- Networking with attendees from industry and academia.
- Opportunity to publicize work, distribute flyers summarizing their research work.

For additional information please contact the Chairs of Student Research Forum:
- Tauhidur Rahman, Florida Int. Univ, USA mdtrahma@fiu.edu
- Ahmed Aziz, Univ. of Tennessee, USA aziz@utk.edu
- Laavanya Rachakonda, Univ. N. Carolina, USA rachakondal@uncw.edu

General queries, please contact isvlsi2024@easychair.org

Paper submission deadlines

Paper Submission Deadline:	**April 1, 2024**
Acceptance Notification:	**April 30, 2024**
Submission of Final Version:	**May 6, 2024**

Selected papers from ISVLSI 2024 will be invited for submission to a journal special issue. The selection process is based on reviewer feedback and quality of conference presentation

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